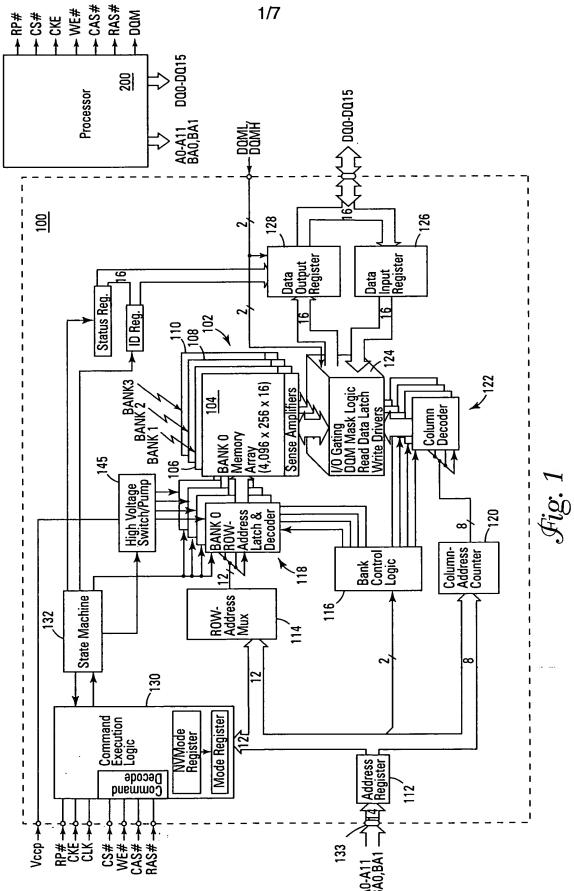
JUN 2 1 2004 TRADENARY

Title: ADJUSTABLE TIMING CIRCUIT OF AN INTEGRATED CIRCUIT

Atty Docket No: 400.002US01
Filing Date: August 25, 2000 Serial No: Serial No: 09/648,857

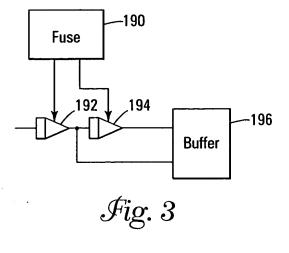




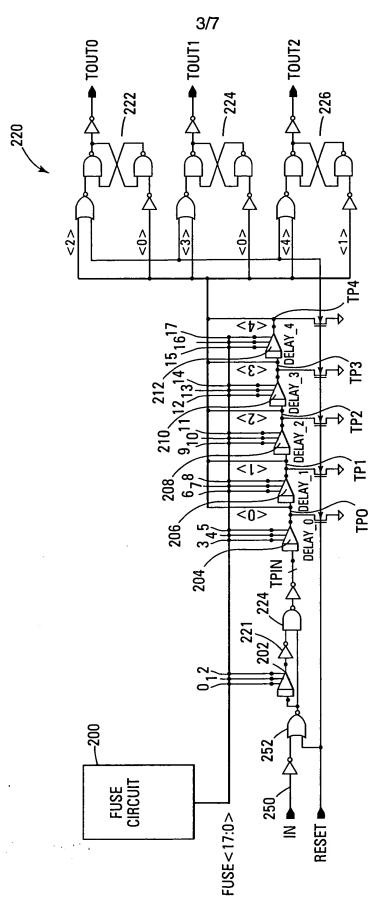
2/7

Vcc 1* D00 2 Vcc0 3 D01 4 D02 5 Vss0 6 D03 7 D04 8 Vcc0 9 D05 10 D06 11 Vss0 12 D07 13 Vcc 14 D0ML 15 WE# 16 CAS# 17 RAS# 18 CS# 19 BA0 20 BA1 21 A10 22 A0 23	<u>150</u>	54
AU :::: 23 A1 :::: 24		32 A7 31 A6
A2 🖽 25		30 H A5
A3 □ 26		29 🞞 A4
Vcc □ 27		<u>28</u>

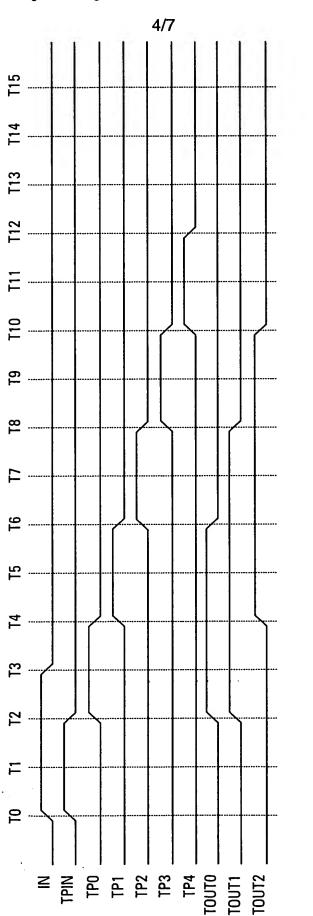
Fig. 2



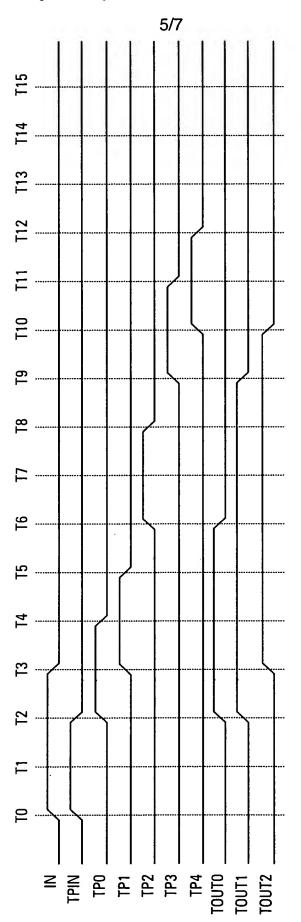












Hig. 6



